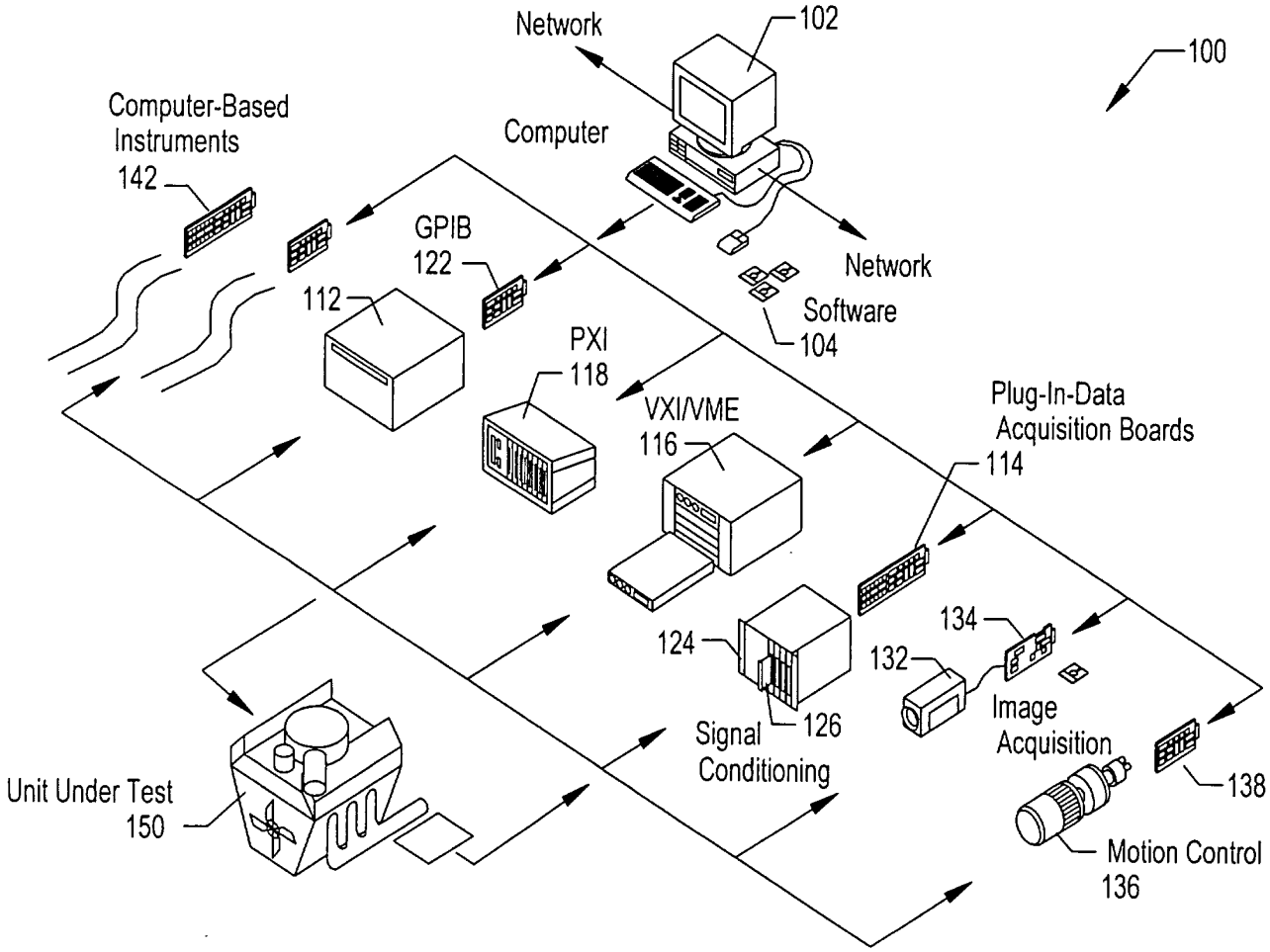
Network
102
100
Computer-Based
Instruments
142
Computer
GPIB
122
Network
112
Software
104
PXI
118
VXI/VME
116
Plug-In-Data
Acquisition Boards
114
124
134
132
Image
Acquisition
Signal
Conditioning
126
138
Unit Under Test
150
Motion Control
136

FIG. 1

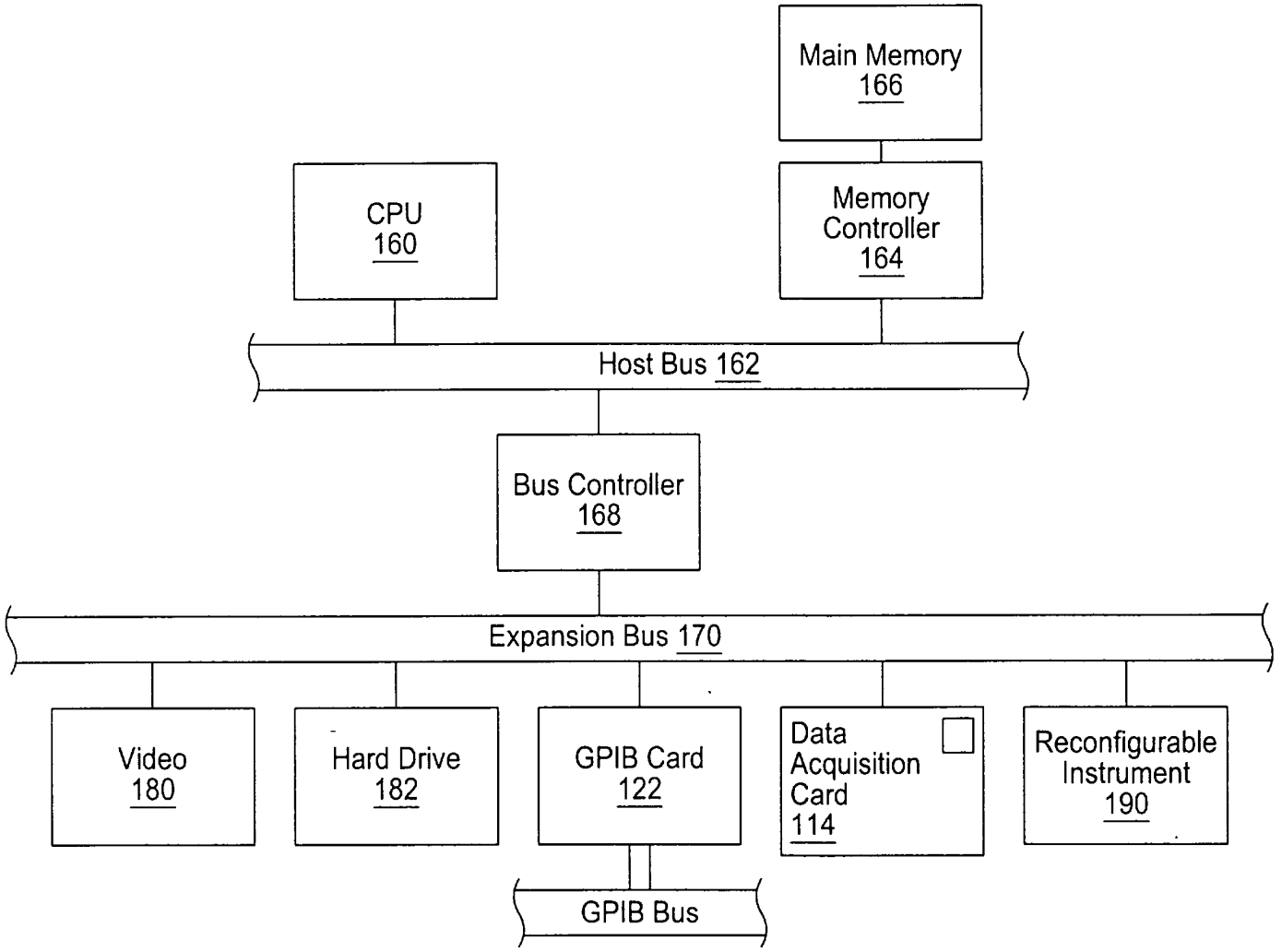
Main Memory
166
CPU
160
Memory
Controller
164
Host Bus 162
Bus Controller
168
Expansion Bus 170
Video
180
Hard Drive
182
GPIB Card
122
Data
Acquisition
Card
114
Reconfigurable
Instrument
190
GPIB Bus

FIG. 2

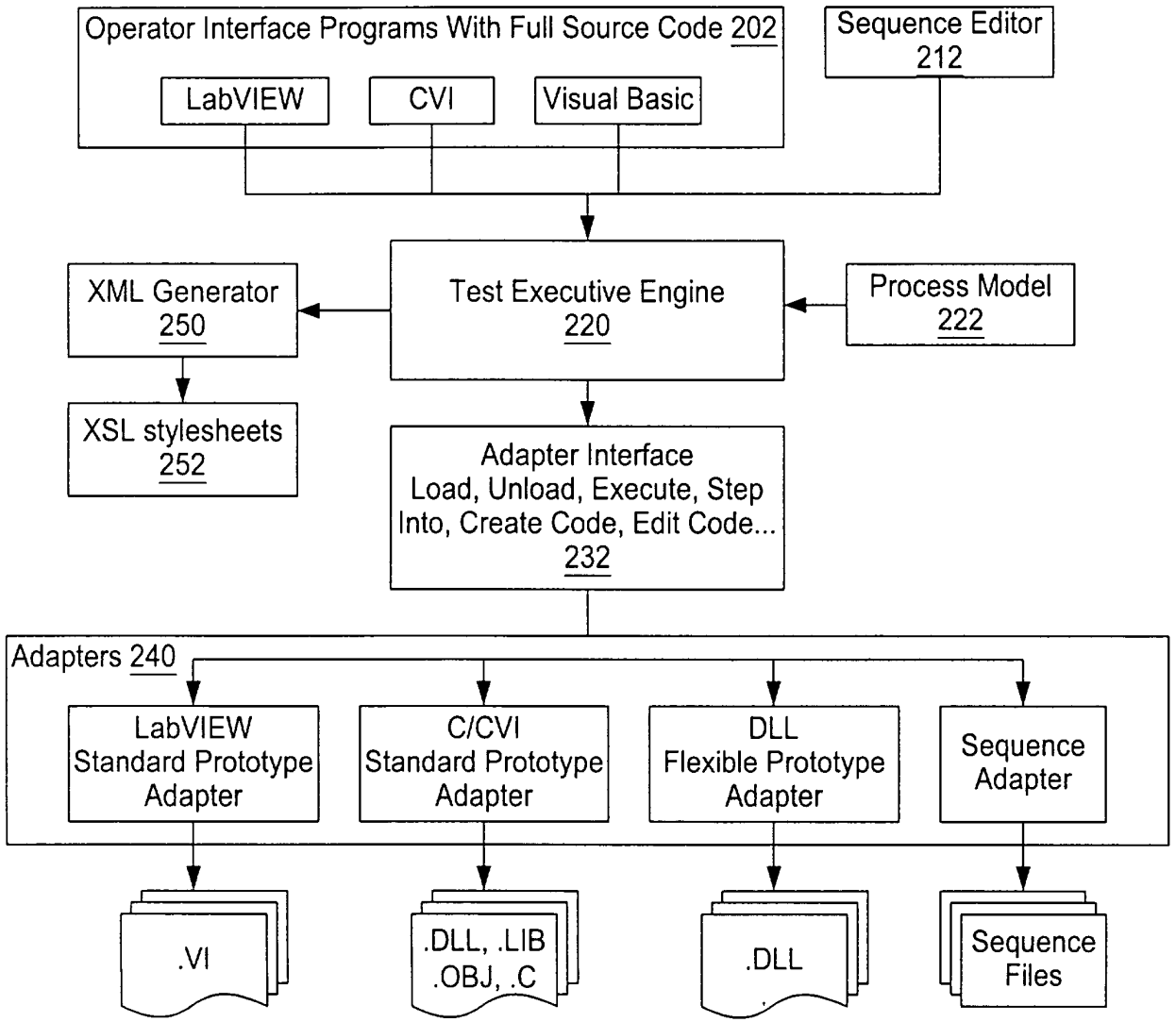

FIG. 3

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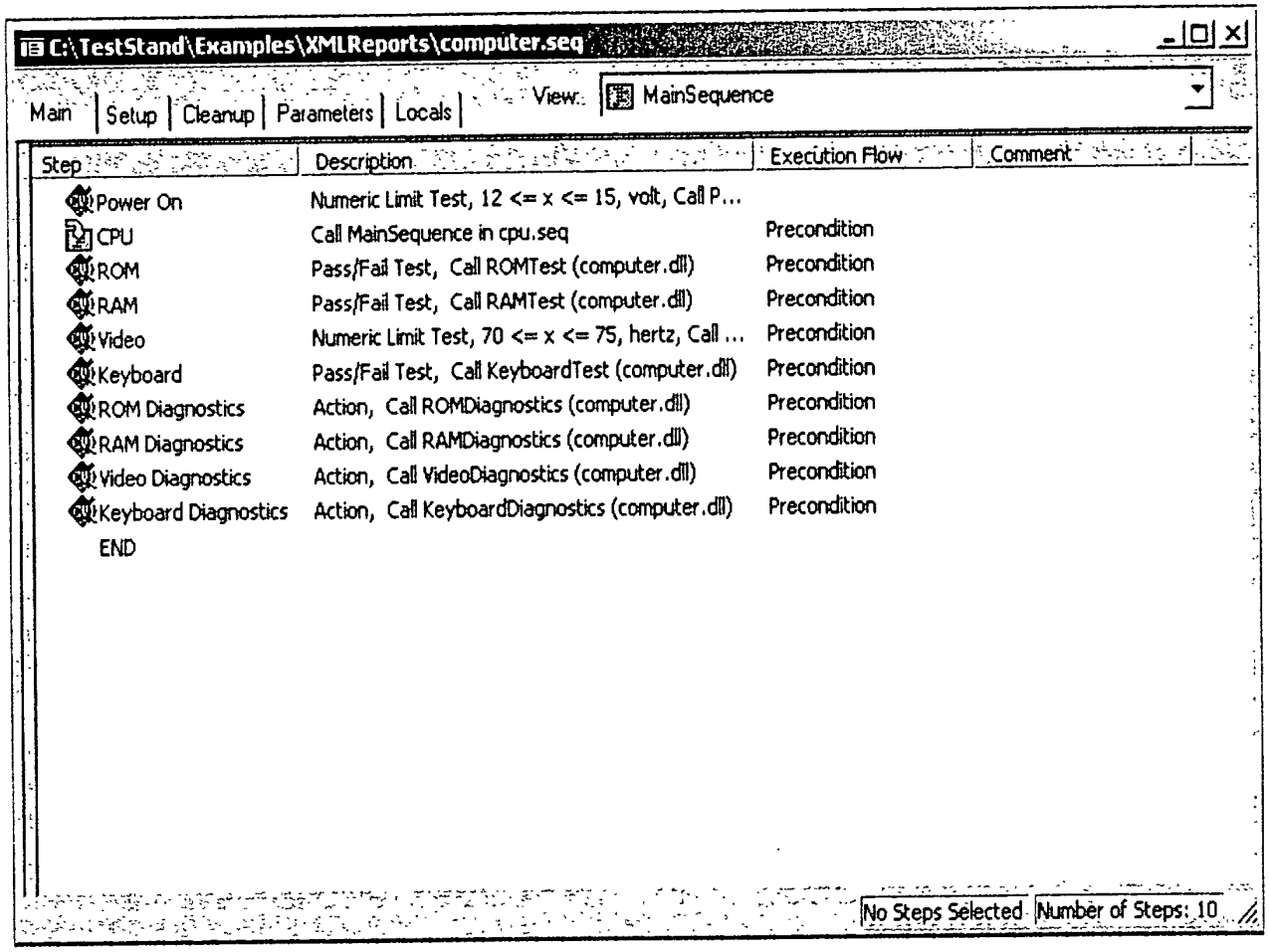
C:\TestStand\Examples\XMLReports\computer.seq
Main
Setup
Cleanup
Parameters
Locals
View:
MainSequence
Step
Description
Execution Flow
Comment
Power On
Numeric Limit Test, 12 <= x <= 15, volt, Call P...
CPU
Call MainSequence in cpu.seq
Precondition
ROM
Pass/Fail Test, Call ROMTest (computer.dll)
Precondition
RAM
Pass/Fail Test, Call RAMTest (computer.dll)
Precondition
Video
Numeric Limit Test, 70 <= x <= 75, hertz, Call ...
Precondition
Keyboard
Pass/Fail Test, Call KeyboardTest (computer.dll)
Precondition
ROM Diagnostics
Action, Call ROMDiagnostics (computer.dll)
Precondition
RAM Diagnostics
Action, Call RAMDiagnostics (computer.dll)
Precondition
Video Diagnostics
Action, Call VideoDiagnostics (computer.dll)
Precondition
Keyboard Diagnostics
Action, Call KeyboardDiagnostics (computer.dll)
Precondition
END
No Steps Selected
Number of Steps: 10

FIG. 4

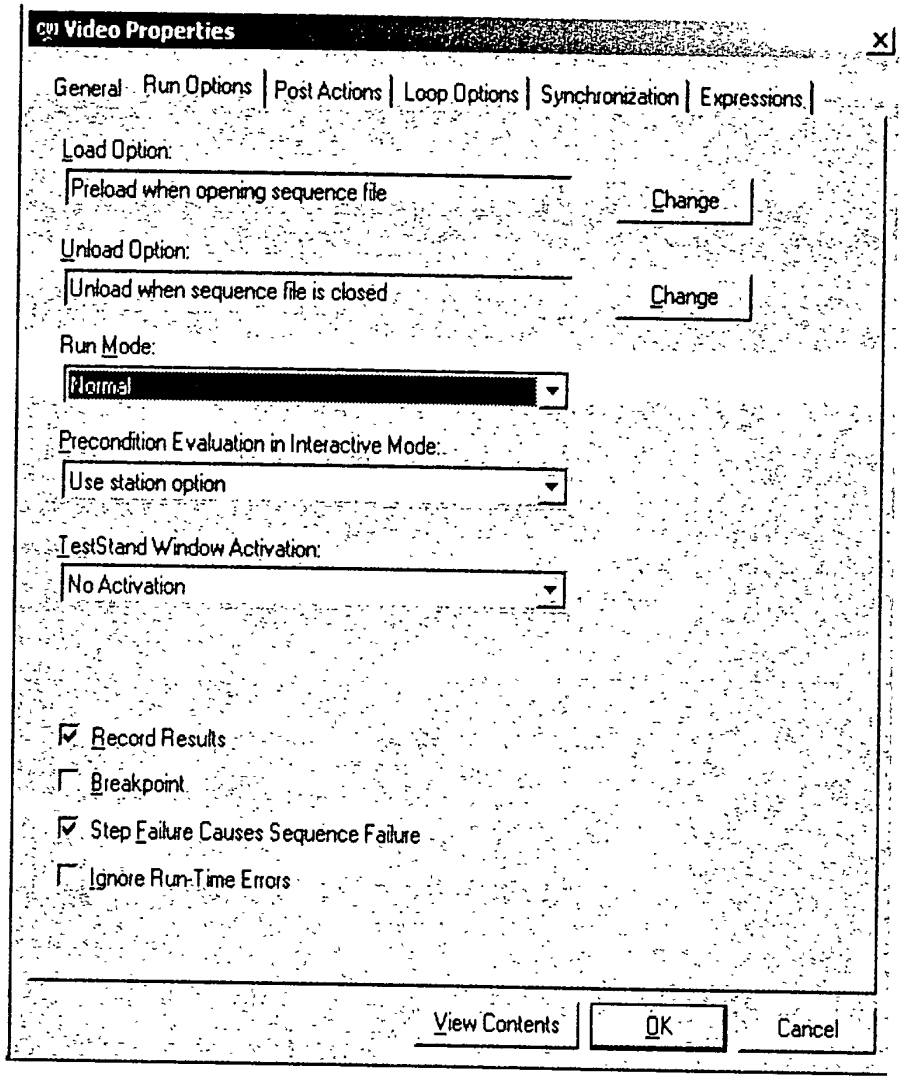

FIG. 5

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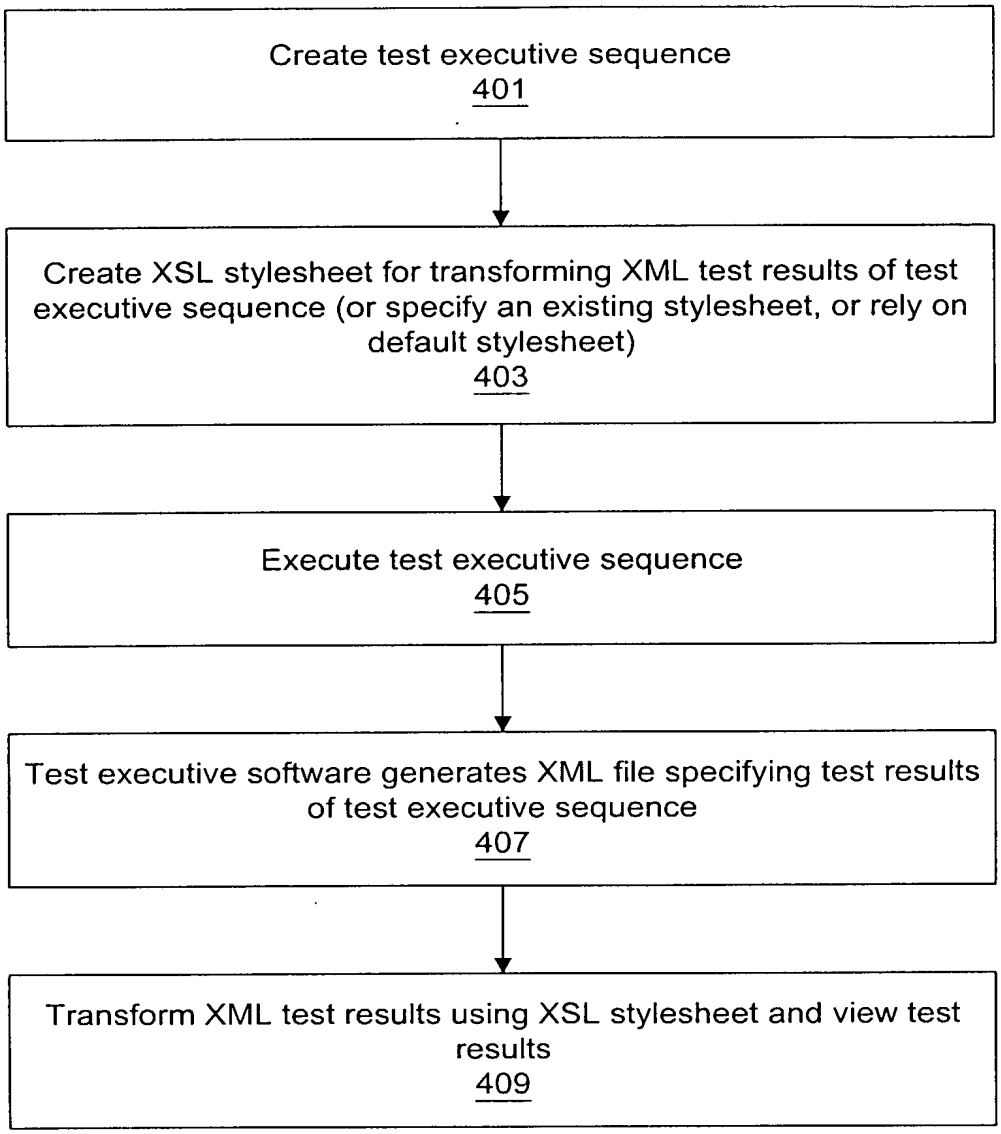

Create test executive sequence
401
Create XSL stylesheet for transforming XML test results of test executive sequence (or specify an existing stylesheet, or rely on default stylesheet)
403
Execute test executive sequence
405
Test executive software generates XML file specifying test results of test executive sequence
407
Transform XML test results using XSL stylesheet and view test results
409

FIG. 6

UUT Report

- **Station ID:** TCASTRO
- **Serial Number:** NONE
- **Socket Index:** 1
- **Date:** Friday, February 09, 2001
- **Time:** 3:21:59 PM
- **Operator:** administrator
- **Number of Results:** 18
- **UUT Result:** Failed
- **Failure Chain:**

RAM	MainSequence	computer.seq

FIG. 7A

Begin Sequence: MainSequence
(C:\TestStand\Examples\XMLReports\computer.seq)

Check If Run From Main Sequence	
Status:	Skipped
Explanation	
Status:	Skipped
Pre-test	
Status:	Done
Power On	
Status:	Passed
Numeric:	15
Units:	volt
Limits.	
Low:	12
High:	15
Comp.	GELE (>= <=)
CPU	
Status:	Passed

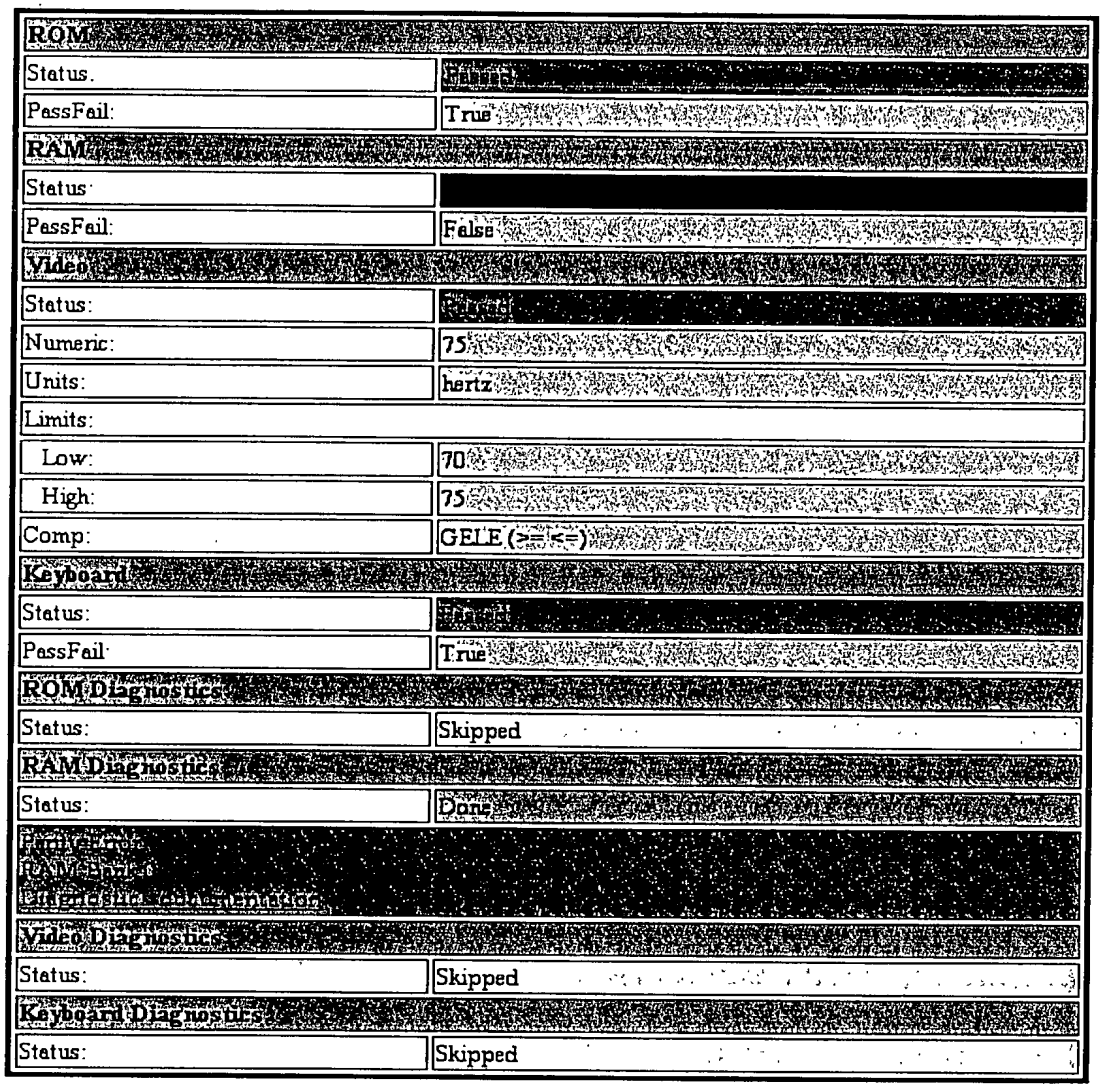
FIG. 7B

Begin Sequence: MainSequence
(C:\TestStand\Examples\XMLReports\cpu.seq)

Pre-test	
Status:	Done
Register	
Status:	[illegible]
PassFail:	True
Instruction Set	
Status:	[illegible]
PassFail:	True
Cache	
Status:	[illegible]
PassFail:	True
FPU	
Status:	[illegible]
PassFail:	True

End Sequence: MainSequence

FIG. 7C



ROM	
Status:	Skipped
PassFail:	True
RAM	
Status:	
PassFail:	False
Video	
Status:	Done
Numeric:	75
Units:	hertz
Limits:	
Low:	70
High:	75
Comp:	GELE (>= <=)
Keyboard	
Status:	Done
PassFail:	True
ROM Diagnostics	
Status:	Skipped
RAM Diagnostics	
Status:	Done
Video Diagnostics	
Status:	Skipped
Keyboard Diagnostics	
Status:	Skipped

End Sequence: MainSequence

End UUT Report

FIG. 7D

UUT Report

- Station ID: TCASTRO
- Serial Number: NONE
- Socket Index: 0
- Date: Friday, February 09, 2001
- Time: 3:21:59 PM
- Operator: administrator
- Number of Results: 18
- UUT Result: Passed

FIG. 7E

Begin Sequence: MainSequence
(C:\TestStand\Examples\XMLReports\computer.seq)

Check If Run From Main Sequence	
Status:	Skipped
Explanation	
Status:	Skipped
Pre-test	
Status:	Done
Power On	
Status:	Passed
Numeric:	15
Units:	volt
Limits:	
Low:	12
High:	15
Comp:	GELE(>=<=)
CPU	
Status:	Passed

FIG. 7F

Begin Sequence: MainSequence
(C:\TestStand\Examples\XMLReports\cpu.seq)

Pre-test	
Status:	Done
Register	
Status:	Passed
PassFail:	True
Instruction Set	
Status:	Passed
PassFail:	True
Cache	
Status:	Passed
PassFail:	True
FPU	
Status:	Passed
PassFail:	True

End Sequence: MainSequence

FIG. 7G

ROM	
Status:	Passed
PassFail:	True
RAM	
Status.	Passed
PassFail:	True
Video	
Status	Passed
Numeric:	75
Units:	hertz
Limits:	
Low.	70
High:	75
Comp:	GELE (>=,<=)
Keyboard	
Status:	Passed
PassFail:	True
ROM Diagnostics	
Status:	Skipped
RAM Diagnostics	
Status:	Skipped
Video Diagnostics	
Status:	Skipped
Keyboard Diagnostics	
Status:	Skipped

End Sequence: MainSequence

End UUT Report

FIG. 7H

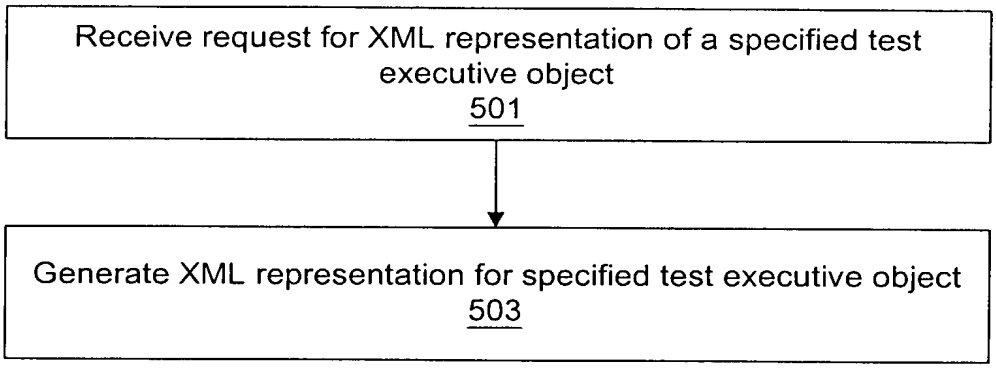

Receive request for XML representation of a specified test executive object
501
Generate XML representation for specified test executive object
503

FIG. 8

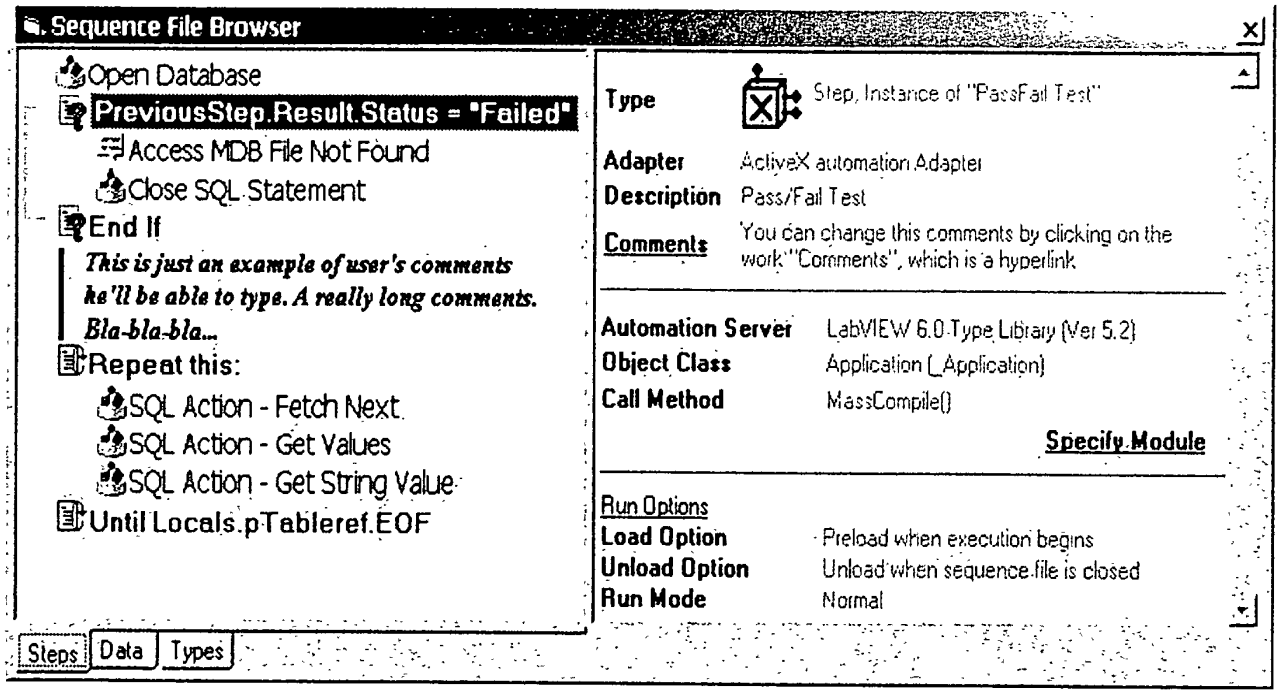


FIG. 9

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